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In Praise of

the fundamentals of circuit analysis with the fundamentals of modern analog and digital integrated circuits I applaud their decision to eliminate from their book the usual mandatory chapter on Laplace transforms, a tool no longer in use by modern circuit designers I

Modul 05: Transistor

buku "Fundamentals of Analog Circuits, 2nd edition" oleh Thomas L Floyd dan David Buchla) Gambar 9: Rangkaian Collector-feedback bias (di-ambil dari buku "Fundamentals of Analog Circuits, 2nd edition" oleh Thomas L Floyd dan David Buchla) Besarnya arus Collector pada rangkaian ini dapat dihitung dengan: $I_C = \frac{V_{CC} - V_{BE}}{R_C + R_B} = DC$ (8) 3

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